

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	09989777
Filing Date	2001-11-19
First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jacob, Mary C.
Attorney Docket Number	CYPR-CD01208M

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	1	CYPR-CD00183; "Capturing Test/Emulation and Enabling Real-Time Debugging Using FPGA for In-Circuit Emulation"; OCT. 10, 2001; U.S. Appl. No. 09/975,104; Snyder	<input type="checkbox"/>
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12	WINTERS; "Using IEEE-1149.1 for in-circuit emulation", Sep. 1994, Proceedings of WESCON '94, ISBN 0-7803-9992-7, pp. 525-528	<input type="checkbox"/>
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15	"POD-Wikipedia, the free encyclopedia"; retrieved on November 14, 2005 from http://en.wikipedia.org/wiki/POD	<input type="checkbox"/>
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17	Wikipedia.org; "Von Neumann architecture"; retrieved from http://en.wikipedia.org/wiki/Von_Neumann_architecture on January 22, 2007; pages 1-5 (from 1207)	<input type="checkbox"/>

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